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ANALYSIS OF DIFFERENT TOPOLOGIES FOR ACTIV POWER FACTOR CORRECTION IN DC – DC CONVERTERS

1. GUNTAKANDLA VIKRAM,2. M RAVINDAR 3 RAMAVATH SHANKAR NAIK

1.Pg Scholar, Department of EEE, SLC's Institute of Engineering and Technology, Hyderabad.

2.Assistant Professor Department Of EEE, SLC's Institute of Engineering and Technology,Hyderabad.

.3.Associate Professor& HOD, Department Of EEE, SLC's Institute of Engineering and Technology,Hyderabad

ABSTARCT

A systematic method for developing isolated buckboost(IBB) converters is proposed in this paper, and single-stagepower conversion, soft-switching operation, and high-efficiencyperformance can be achieved with the proposed family of converters.On the basis of a nonisolated two-switch buck-boost converter,the proposed IBB converters are generated by replacingthe dc buck-cell and boost-cell in the non-IBB converter with theac buck-cell and boost-cell, respectively. Furthermore, a family ofsemiactive rectifiers (SARs) is proposed to serve as the secondaryrectification circuit for the IBB converters, which helps to extendthe converter voltage gain and reduce the voltage stresses on thedevices in the rectification circuit.Hence, the efficiency is improvedby employing a transformer with a smaller turns ratio and reducedparasitic parameters, by using low-voltage rating MOSFETs anddiodes with better switching and conduction performances. A fullbridgeIBB converter is proposed and analyzed in detail as anexample. The phase-shift modulation strategy is applied to thefull-bridge IBB converter to achieve IBB conversion. Moreover,soft-switching performance of all active switches and diodes can beachieved over a wide load and voltage range by the proposed converterand control strategy. A 380-V-output prototype is fabricatedto verify the effectiveness of the proposed family of IBB converters,the SARs, and the control strategies.

I INTRODUCTION

In this chapter, we derive the dynamic models of DC-to-DC power converters.The

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most elementary structures of these converters are broadly classi¯ed into*second order converters* and *fourth order converters*. In attention to the number of independent switches they are classed into two groups: *mono-variable*,or *Single Input Single Output* (SISO), and *multi-variable*, or *Multiple InputMultiple Outputs* (MIMO). The most commonly used converters correspondto the SISO second order converters. The advantages and di±culties of theMIMO converters is just beginning to be fully understood. We remark thatthere are converters with *multiple dependent* switches. These may still be SISOor MIMO. The second order converters that we study in this book are: theBuck converter, the Boost converter, the Buck-Boost converter and the non-inverting Buck-Boost converter. The fourth order converters are: the C¶ukconverter, the Sepic converter, the Zeta converter and the quadratic Buckconverter. Some multi-variable converters can be obtained by a simple cascade arrangement of the basic SISO converter topologies while considering theswitch in each stage as being completely independent of the other switchespresent in the arrangement. Many books in the Power Electronics literaturepresent derivations of the power converters models. For a rather thoroughpresentation of the Euler-Lagrange modelling technique in DC-to-DC powerconverters, the reader is referred to the book by Ortega *et al.* .The au-thors ¯nd the pioneering book by Severns and Bloom quite accessibleand direct. The thoughtful book by Kassakian *et al*contains also de-tailed derivations of the most popular DC-to-DC power converters topologies.Standard reference textbooks, which do contain models of DC-to-DC powerconverters but with a special emphasis on the steady state PWM switched

We extensively use, in the derivation of the dynamic controlled modelsof the several converters, the fundamental Kircho®'s current and Kircho®'s

2 Modelling of DC-to-DC Power Converters

voltage laws. The methodology for the derivation of the models is, therefore,quite straightforward.We ¯x the position of the switch, or switches, and derivethe

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di®erential equations of the circuit model. We then combine the derivedmodels into a single one parameterized by the switch position function whosevalue must coincide, for each possible case, with the numerical values of either\zero" or \one". In other words, the numerical values ascribed to the switchposition function is the binary set *f*0*;* 1*g*. The obtained switched model is theninterpreted as an *average model* by letting the switch position function takevalues on the closed interval of the real line [0*;* 1]. This state averaging procedure has been extensively justi¯ed in the literature since the early days ofpower electronics and, therefore, we do not dwell into the theoretical justi¯cations of such averaging procedure. The consequences of this idealization will

not be counterproductive in the controller design procedure, nor in its actualimplementation through Pulse Width Modulated (PWM) \electronic actuators" or its corresponding sliding mode counterparts. In order to simplify theexposition, we make no distinction between the average model variables andthe switched model variables. At the beginning, we shall only distinguish between these models by using *uav* for the control input variable in the averagemodel and by using *u* for the switched model. In later chapters, we shall alsolift this distinction. It will be clear from the context whether we are referringto the average or to the switched model.

3 The Buck Converter 13

variables. Naturally, as long as actual laboratory implementation goes, thenormalization considerably simpli¯es the controller design but the obtain de-sign cannot be directly implemented. The actual gain values and expressionsin the derived controllers have to be naturally \de-normalized" (i.e., placed inoriginal physical units) before the implementation. We believe such an extrae®ort is worth the pain.In the exposition about each converter, average models are utilized inestablishing the average values of the equilibrium points. We usually parameterize the derived equilibrium points in terms of the desired average normalized value of the output voltage. Other parameterizations are still possibleand, in fact, the normalized model equations allow us to carry them out withrelative ease. The nature of the parametrization of the equilibrium points usually determines the fundamental characteristic of the converter in the sense that its static features dene the amplifying, attenuating, or even both, features present in a specic converter. We refer to the static average normalizedinput-output relation as the *static transfer function*. This quantity is readilyobtained from the average input value parametrization of the desired equilibrium output voltage.

IV INVERTING BUCK-BOOST CONVERTER TOPOLOGY

A buck converter decreases an input voltage. At least one switch at the input is required to connect the input voltage to one side of the inductor. Another switch at the same side of the inductor switches to ground in the off state or alternatively, a diode takes over the decreasing inductor current. The other side of the inductor is permanently connected to the output. Acapacitor has to be in place at the input and at the output for stability reasons and to limit huge voltage drops upon fast load transients. A boost converter increases an inputvoltage. At least one switch at the output is required to connect one side of the inductor to ground. Another switch at the same side of the inductor switches to the output in the off state or alternatively, a diode takes over the decreasing inductor current. The other side of the inductor is permanently connected to the input. A buck-boost converter basically is a combination of a buck and a boost converter. There are normally two switches at the input and two switches at the output. It can either increase or decrease the input voltage. An inverting buck-boost converter has only one switch at the input and one switch or a diode at the output. But, to be honest, since integrated circuits usually cannot handle negative voltages, the switch at the output cannot be used. The diode becomes a necessity. Therefore, sometimes a slightly modified boost converter is used and another inductor and another capacitor are arranged as shown in fig. 1 to generate a negative output voltage. Figure.1. Simple buck-boost converter topology The inverter discussed in this paper does not require two inductors but uses the simple one inductor

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concept shown in fig. 1. Diode D1 just indicates that there is a parasitic pn-junction associated with the PMOS switch S1 which is the only component in figure that is on chip. The rest are all external components. D2 is expected to be a schottky diode. Rout represents the load which could be replaced by a current source Iload. S1 has to be controlled such that the desired voltage VOUT remains stable under all VIN and Iload conditions. In continuous current mode CCM operation the inductor current never reaches zero or goes below zero. S1 is turned on and off with a constant frequency

V.SIMULATION RESULTS:

Fig : Model File:

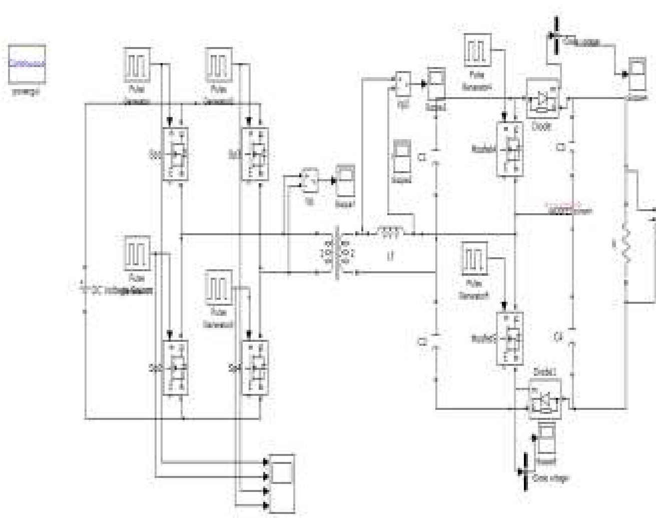
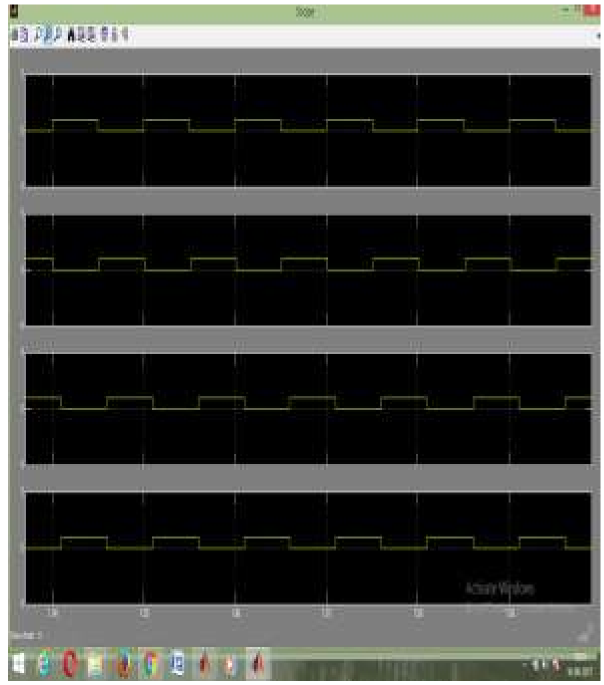
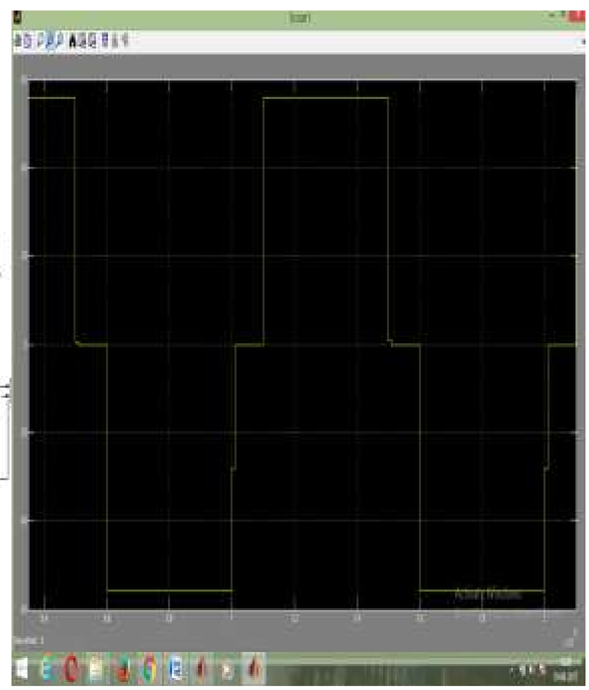


Fig: SImulation Wave forms





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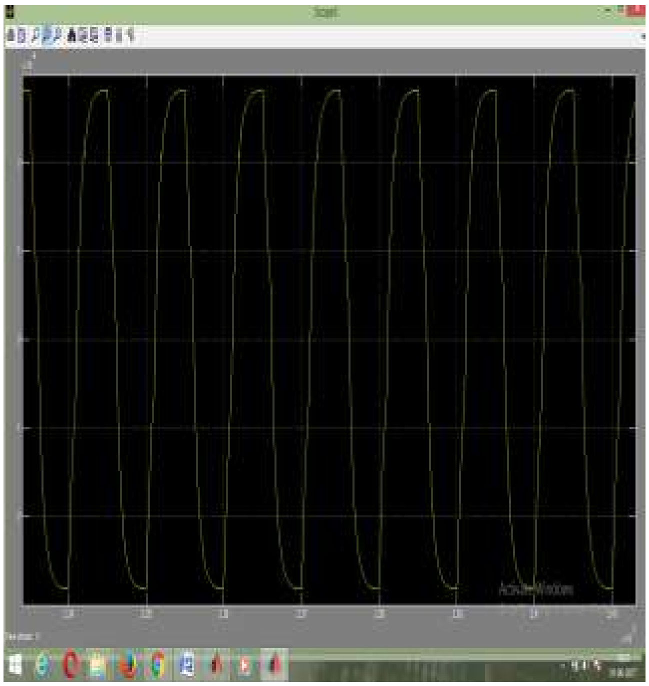
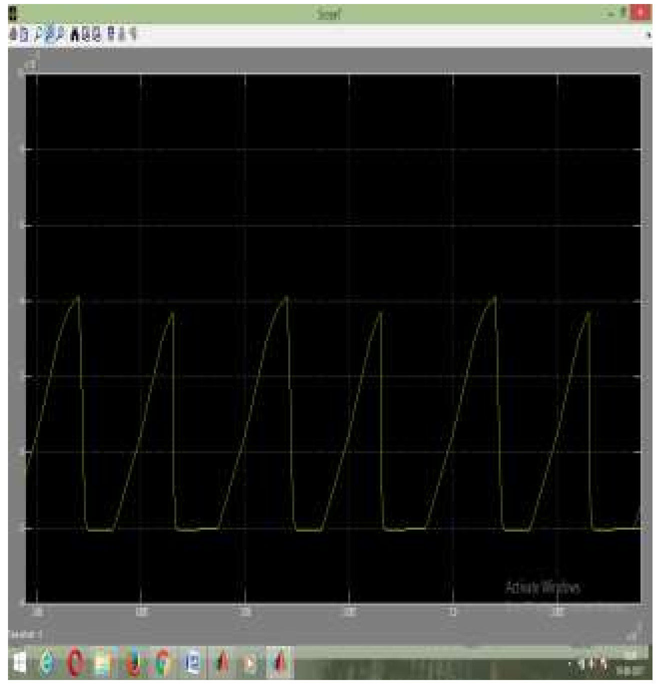
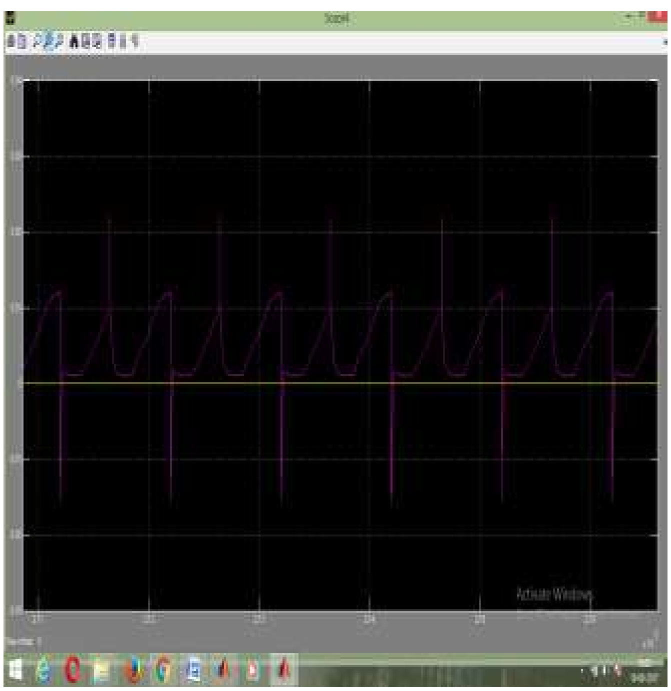
Vlf







The Figs. show the experimental waveforms of the proposed converter in the boost mode. The waveforms in above fig are tested under 40-V input voltage with the normalized voltage gain G > 1. The waveforms in Fig. 22 are tested under 48-V input voltage with the normalized voltage gain G = 1. It can be seen that, when the input voltage is 48 V and the voltage gain G = 1, the inductor current iLf remains constant during the power-transferring state, because the voltage applied to the inductor is nearly zero. When Vin = 40 V and G > 1, the inductor current iLf decreases during the power-transferring state. The amplitude of the secondary-side square wave voltage vs is only 95 V, which is a quarter of the output voltage. The voltage can be stepped up to 380 V from 40 V by using a transformer with a small turns ratio n = 2. The experimental waveforms demonstrate the theoretical analysis pretty well. The ZVS waveforms of the primary-side active switch SP 1 and the secondary-side switch S1 are shown in above fig. Since all the primary-side switches work in the same pattern and boththe secondary-side switches work symmetrically, ZVS is accomplished for all the primary-side and secondary-side active switches.

VI CONCLUSION

A novel family of IBB converters has been proposed and investigatedin this paper. The IBBs are based on the nonisolatedtwo-switch buck-boost converter, and generated by replacingthe dc buck-cell and boost-cell in the nonisolated two-switchbuck-boost converter by an ac buck-cell and boost-cell. SARsare developed by merging a half-bridge circuit and a switchedcapacitorcircuit, and used as the boost-cell in the IBB converterfor high-output voltage applications. The voltage stresses on thedevices in the SAR are reduced significantly, and hence, lowvoltagerating devices with better conduction and switching performancehave been used to improve efficiency. Furthermore,ZVS and ZCS have been achieved for all active switches anddiodes, respectively, by adopting the phase-shift modulation.Operating principles, output characteristics, and soft switchingperformance of a novel FB-IBB converter are presented in detail.The analysis and performance have been fully validatedexperimentally on a 40–56-V input, 380-V output hardwareprototype. Experimental results demonstrate that the proposedIBB converter is an excellent candidate for high efficiency IBBconversion in high-output voltage applications.

REFERENCES

[1] R. Gules, J. De Pellegrin Pacheco, H. Le˜aes Hey, and J. Imhoff, “Amaximum power point tracking system with parallel connection for PVstand-alone applications,”

IEEE Trans. Ind. Electron., vol. 55, no. 7,pp. 2674–2683, Jul. 2008.

[2] J. Zeng, W. Qiao, L. Qu, and Y. Jiao, "An isolated multiport DC-DCconverter for simultaneous power management of multiple different renewableenergy sources," *IEEE J. Emerg. Sel. Topics Power Electron.*, vol.2, no. 1, pp. 70–78, Mar. 2014.

[3] H. Wu, K. Sun, R. Chen, H. Hu, and Y. Xing, "Full-Bridge three-portconverters with wide input voltage range for renewable power systems,"*IEEE Trans. Power Electron.*, vol. 27, no. 9, pp. 3965–3974, Sep. 2012.

[4] Z. Guo, D. Sha, and X. Liao, "Input-series-output-parallel phase-shiftfull-bridge derived DC-DC converters with auxiliary LC networks toachieve wide zero-voltage switching range," *IEEE Trans. Power Electron.*,vol. 29, no. 10, pp. 508–513, Oct. 2014.

[5] H. Wu and Y. Xing, "Families of forward converters suitable for wideinput voltage range applications," *IEEE Trans. Power Electron.*, vol. 29,no. 11, pp. 6006–6017, Nov. 2014.

[6] D. S. Gautam, F. Musavi,W. Eberle, andW. G. Dunford, "A zero-voltageswitching full-bridge DC-DC converter with capacitive output filter forplug-in hybrid electric vehicle battery charging," *IEEE Trans. Power Electron.*,vol. 28, no. 12, pp. 5728–5735, Dec. 2013.

[7] Y. Wang, W. Liu, H. Ma, and L. Chen, "Resonance analysis and softswitchingdesign of isolated boost converter with coupled inductors forvehicle inverter application," *IEEE Trans. Power Electron.*, vol. 30, no. 3,pp. 1383–1392, Mar. 2015.

[8] H. Kim, C. Yoon, and S. Choi, "An improved current-fed ZVS isolatedBoost converter for fuel cell applications," *IEEE Trans. Power Electron.*,vol. 25, no. 9, pp. 2357–2364, Sep. 2010.

[9] Y. Zhao, W. Li, Y. Deng, and X. He, "Analysis, design, and experimentationof an isolated ZVT boost converter with coupled inductors," *IEEETrans. Power Electron.*, vol. 26, no. 2, pp. 541–550, Feb. 2011.

[10] H. Keyhani and H. A. Toliyat, "Partial-resonant buck-boost and flybackDC-DC

converters," *IEEE Trans. Power Electron.*, vol. 29, no. 8,pp. 4357–4365, Aug. 2014.

[11] C. Yao, X. Ruan, X. Wang, and C. K. Tse, "Isolated buck-boost DC/DCconverters suitable for wide input-voltage range," *IEEE Trans. PowerElectron.*, vol. 26, no. 9, pp. 2599–2613, Sep. 2011.

[12] C. Yao, X. Ruan, and X.Wang, "Automatic mode-shifting control strategywith input voltage feed-forward for full-bridge-boost DC-DC convertersuitable for wide input voltage range," *IEEE Trans. Power Electron.*,vol. 30, no. 3, pp. 1668–1682, Mar. 2015.

[13] C. Konstantopoulos and E. Koutroulis, "Global maximum power pointtracking of flexible photovoltaic modules," *IEEE Trans. Power Electron.*,vol. 29, no. 6, pp. 2817–2828, Jun. 2014.

AUTHOR'PROFILE:

Guntakandla Vikram received the B.Tech degree in Electrical and Electronics Engineering from TKR College of Engineering and Technology 2014 from JNTU, Hyderabad, Telangana, India and He is pursuing M.Tech in Electrical Power Systems(EPS) as specialization in the Department of Electrical and Electronics Engineering, SLC's Institute of Engineering and Technology, Affiliatedto theJNTU Hyderabad, Telangana, India. His research interests include in the area of applications based on high output voltages and low output voltges using different semiconductor devices.

E-mail id: vikram.12954@gmail.com

Ravindar Moodu completedB.Tech. in Electrical & Electronics Engineering in 2007 from JNTU, Hyderabad, Telangana, India and M.Tech in Electrical Power System(EPS) in 2016 from JNTUH,Hyderabad, Telangana, India., He is currently an Assistant Professor with the Department of Electrical&Electronic Engineering at SLC College Jntu University,Hyderabad, Telangana India. His research interests include the design and control of

power converters, soft-switching power converters, resonant power circuits, photo voltaic systems, power factor correction, switched-mode power supply, induction heating circuits, and electromagnetic-interference suppression. Power System ,Power Electronics, power quality, renewable energy Resource, Hvdc FACTS Multilevel Inverter Topology.

E-mail id: ravi32foryou@gmail.com

Ramavath Shankar Naik received the B.Tech degree in Electrical Engineering from Vignan's Engineering College, Vadlamudi, Guntur, India, in 2006. He Completed M.E in Electrical Engineering with Power system Automation as specialization in the Department of Electrical Engineering, Andhra University College of Engineering (Autonomous), Visakhapatnam, He is currently an Associate Professor with the Department of Electrical &Electronic Engineering at SLC's Institute of Engineering and Technology, JNTU University, Hyderabad, Telangana, India. His area of interest lies in Power system operation and control, optimal operation of power systems and FACTS.

E-mail id: shankar02234@gmail.com